

The CMS electromagnetic calorimeter barrel upgrade for High-Luminosity LHC

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The High Luminosity LHC (HL-LHC) will provide unprecedented instantaneous and integrated luminosity. The lead tungstate crystals forming the barrel part of the CMS Electromagnetic Calorimeter (ECAL) will still perform well, even after the expected 3000 fb⁻¹ at the end of HL-LHC. The avalanche photodiodes (APDs) used to detect the scintillation light have recently been exposed to the levels of radiation expected at the end of HL-LHC. Although they will continue to be operational, there will be some increase in noise due to radiation-induced dark-currents. Triggering on electromagnetic objects with 140 pileup events necessitates a change of the front-end electronics. New developments in high-speed optical links will allow single-crystal readout at 40 MHz to upgraded off-detector processors, allowing maximum flexibility and enhanced triggering capabilities. The very-front-end system will also be upgraded, to provide improved rejection of anomalous signals in the APDs as well as to mitigate the increase in APD noise. We are also considering lowering the ECAL barrel operating temperature from 18 degrees C to about 8-10 degrees C, in order to increase the scintillation light output and reduce the APD dark current.

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1. Introduction

The Compact Muon Solenoid (CMS) [1] detector was originally designed to operate for about ten years, for LHC instantaneous/integrated luminosities of $1x10^{34}$ cm⁻² s⁻¹ and 500 fb⁻¹ respectively. The High Luminosity LHC (HL-LHC) will provide an instantaneous luminosity of $5x10^{34}$ cm⁻² s⁻¹; CMS will accumulate an integrated luminosity of 3000 fb⁻¹ by about 2035 [2]. Requirements on the triggering system of CMS will necessitate changes in the on-detector electronics of the ECAL. Upgrading these electronics also allows for upgrades to improve performance in this challenging environment.

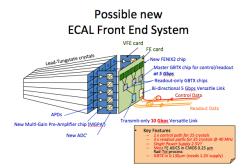
1.1 CMS electromagnetic calorimeter

The CMS electromagnetic calorimeter (ECAL) is a hermetic, homogenous, high granularity and high resolution detector composed of PbWO4 scintillating crystals [1]. The barrel portion of the ECAL ($|\eta|$ |<1.479) is made up of 61200 crystals, with the scintillation light read out by two avalanche photodiodes (APD) in parallel, mounted on the rear face of the crystals. The signals from groups of 5 crystals are processed by a very front-end card (VFE) which performs the preamplification and analog to digital conversion. Five VFEs are connected to each front-end (FE) card.

Figure 1

The Existing ECAL Front End System VFE card FE card FEXIX chip in "STRIP" mode FEXIX chip in "DAQ" mode FEXIX chip in "TOP mode Gigabit Links (GOH) Readout Data Trigger Data Clock & Control chip (CCU) - Key Features - Single Power Supply 2.5V - All FE ASICS in CMOS 2.5 ym Rad-10 process

(a) Current ECAL Barrel readout electronics design. Signal shaping is done by the very front-end (VFE) card. Currently trigger primitives are generated by the front-end (FE) card. Separate readout for trigger and data. Maximum trigger rate out from FE is 150 kHz set by the current level 1 trigger accept.



(b) Proposal for upgraded ECAL Barrel readout electronics design. Signal shaping is done by the very front-end (VFE) card. Front-end (FE) card sends single crystal information off-detector at 40MHz. Trigger rate can be increased to 1MHz required by upgraded level 1 trigger accept.

Trigger primitives are calculated by the FE by summing the energy in a 5x5 array of crystals and this sum is sent to the central CMS trigger system at 40MHz. Data are read out at the single crystal (61200 channels) level at a maximum rate of 150kHz. There are two optical links on the FE card—one for data readout and one for trigger readout.

1.2 Motivation for ECAL barrel electronics upgrade

The components of the ECAL barrel can survive the HL-LHC radiation environment, but the electronics cannot meet the triggering requirements of the CMS detector for this run period (see

section 2). Replacement of the electronics allows opportunities to improve the performance of the ECAL barrel in the high pileup and high luminosity environment of the HL-LHC.

2. ECAL barrel trigger for HL-LHC

The CMS trigger will have a level 1 accept rate of 1MHz and require a latency of up to $20~\mu s$. The current FE card cannot satisfy these requirements. By replacing the FE, the optical links, and the off-detector electronics the ECAL will use the same data stream for trigger and data, sending all 61200 channels off detector at 40MHz. This allows the trigger primitives to be calculated off-detector. The new electronics will benefit from development of the Versatile Link and GBTX at CERN. Figure 1b shows a possible upgrade of the electronics.

3. Improved online anomalous event mitigation

Anomalous events (spikes) are energy deposits directly into the bulk of the APD (instead of interacting and scintillating in the crystal first). The spike rate is proportional to instantaneous luminosity. The spike pulse shape is shorter in time than the normal scintillation signal and also produces a signal in only one APD whereas an electromagnetic shower typically deposits at least 15% of its energy in neighboring crystals [1].

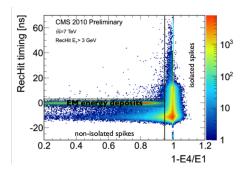


Figure 2: The two main techniques currently used for spike filtering: timing cuts measured from pulse shape and shower shape variable comparing energy in most energetic crystal to its four neighbors.

Being able to improve the spike filtering at the trigger level will reduce the fake trigger rate for ECAL at the HL-LHC considerably. Upgrading the VFE will allow timing (currently used offline) to be improved and used at the trigger level to veto spikes. The full event readout at 40MHz will allow the shower shape variable (currently used offline) to be used at the trigger level. This allows the ECAL to minimize the effect of spikes at the trigger level using techniques developed offline in run 1.

4. Pileup mitigation

In the high-pileup environment of the HL-LHC the current pulse shaping in ECAL will be distorted by pileup events from previous bunch crossings. The pulse shape produced by the VFE

in ECAL is 42ns, while the bunch spacing is 25ns at the HL-LHC. Energy deposited in a crystal from earlier bunch crossings could contaminate the signal. Shortening the signal shaping time by upgrading the VFE can help to mitigate this out-of-time pileup effect.

5. APD noise and cooling

The APDs have intrinsic noise due to the dark current in the silicon bulk from irradiation damage proportional to the square root of integrated luminosity. Extrapolations from run 1 show the noise level at the end of the HL-LHC is 400MeV per crystal. After 1000 fb⁻¹ this noise dominates the energy resolution. It is possible to decrease the dark current by a factor of 2 by cooling the ECAL from 18° C to 8° C. By upgrading the VFE and shortening the signal shaping time the noise will also decrease.

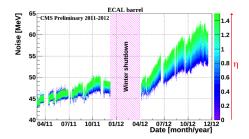


Figure 3: Noise in ECAL barrel during run 1. Noise scales as the square root of integrated luminosity. Conversion from ADC counts to MeV includes degradation of light yield due to crystal transparency loss.

6. Conclusion

The HL-LHC will necessitate changes to the ECAL barrel electronics. The FE must be upgraded to allow single crystal readout at 40MHz (full ECAL granularity for triggering). All buffers and triggering are moved off detector, which will allow future proofing. Applying these upgrades also provides an opportunity to improve performance in the high luminosity and high pileup environment. Upgrading the VFE will allow shorter signal shaping time to mitigate pileup and noise. The anomalous signal filter used at trigger level can also be improved. The planned upgrades to the CMS ECAL Barrel will allow the CMS ECAL to continue functioning well during the extreme conditions of the HL-LHC.

References

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