

The Gain Stabilization of SiPM

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We have designed and developed the adaptive power supply to keep the gain stability of SiPMs in the Analog Hadron Calorimeter of the CALICE ILC detector stable at the level of 1% . This power supply compensates automatically the bias voltage for temperature variations in the range of 5 – 40 °C. It operates in the bias voltage range 15 – 90 V suitable for most of SiPMs on the market. We demonstrate the performance of the gain stability with a prototype that was produced in industry after measurements with a test board. We checked the gain stability for several types of SiPMs from three producers – Hamamatsu, KETEK and CPTA.

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We worked with SiPMs from three manufacturers: CPTA, Hamamatsu and KETEK (the list of SiPMs and MPPCs is given in Tab. 1). We utilize the default T3B calibration procedure [1] to extract the single photo-electron spectra of SiPMs. DAQ includes preamplifier and 1.25 Gsps digital oscilloscope (picoscope) [2], which was set to acquire 50,000 events per run. The algorithm identifies the signal of a single photo-electron and it is subsequently capable of decomposing any SiPM signal into the cumulative signal of single photo-electrons. SiPMs were illuminated by blue LED driven from a pulse generator.

We used three thermistors PT1000 to monitor ambient temperature around SiPM. Another temperature sensor LM35D (Texas instruments) placed close to a SiPM under the test provides a signal 10mV/°C to the regulator for the correction of the output bias voltage.

| Manufacturer and Type # | Sensitive area [mm ²] | Pixel pitch [μm] | #pixels | Typical V_{bias} [V] | Typical G [$\times 10^5$] | Serial # |
|-------------------------|-----------------------------------|------------------|---------|------------------------|-------------------------------|----------|
| Hamamatsu | | | | | | |
| S10943-8584(X) | 1 × 1 | 50 | 400 | 71.69 | 7.49 | 11759 |
| S10943-8584(X) | 1 × 1 | 50 | 400 | 71.57 | 7.49 | 11766 |
| S10943-8584(X) | 1 × 1 | 50 | 400 | 71.50 | 7.48 | 11770 |
| S10943-8584(X) | 1 × 1 | 50 | 400 | 71.33 | 7.48 | 11771 |
| Sample A | 1 × 1 | 20 | 2500 | 66.7 | 2.3 | A1 |
| Sample B | 1 × 1 | 20 | 2500 | 73.3 | 2.3 | B1 |
| Sample A | 1 × 1 | 15 | 4440 | 67.2 | 2.0 | A2 |
| Sample B | 1 × 1 | 15 | 4440 | 74.0 | 2.0 | B2 |
| CPTA | | | | | | |
| | 1 × 1 | 40 | 796 | 33.4 | 7.1 | 857 |
| | 1 × 1 | 40 | 796 | 33.1 | 6.3 | 922 |
| | 1 × 1 | 40 | 796 | 33.3 | 6.3 | 975 |
| | 1 × 1 | 40 | 796 | 33.1 | 7.0 | 1065 |
| | 1 × 1 | 40 | 796 | 33.3 | 14.6 | 1677 |
| KETEK | | | | | | |
| MP15 V6 ? | 2(1.2 × 1.2) | 15? | ? | -28 | ? | W8 |
| MP20 V4 ? | 3 × 3 | 20? | ? | -28 | ? | W12 |

Tab. 1. A list of SiPM (MPPC) we tested (left).

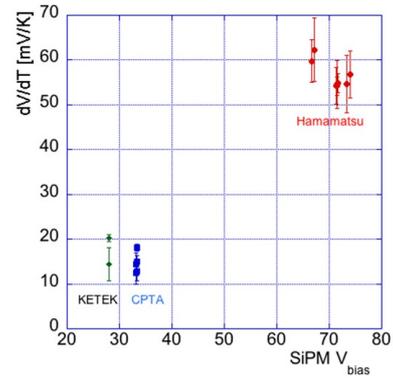


Fig. 2. The compensation slope dV_{bias}/dT versus bias V_{bias} for three types of SiPMs (right).

All measurements were performed in the temperature range of 10 to 30°C. The results of the gain dependence on temperature and bias voltage were recalculated to the dV_{bias}/dT compensation slope for each SiPM and were used in the design of the adaptive bias voltage regulator test board. After the circuit construction, we measured with it a subset of four SiPMs/MPPCs again in the extended temperature range 5 - 40°C. These were Hamamatsu #11759, CPTA #857 and #1677 and KETEK #W12.

4. Adaptive power regulator

The power part of the analogue HV regulator uses two HV MOSFETs in a totem pole configuration. In the control part, the output voltage is divided by ten using precision resistors and attached to the feedback input of the error amplifier. The reference input of the amplifier is connected to a multi-turn potentiometer which is connected to a precision voltage reference LT1021-10. The voltage reference is trimmed to 10.000 V and corresponds to 100 V at the output of the regulator. Multi-turn potentiometer sets output voltage in range 15 - 100 V. The error amplifier drives the gate of the lower MOSFET. Loop stability is achieved by means of a third order compensation network. Temperature sensor LM35D (Texas instruments) placed close to SiPM under the test provides a signal 10 mV/°C for the correction of the output voltage. We used analogue design which omits problems with nonlinearity of ADC/DAC conversion. In

any case for this application an equivalent of effective 16 bits resolution is needed. The design can be extended to the control from a PC with a DAC module to set the nominal output voltage.

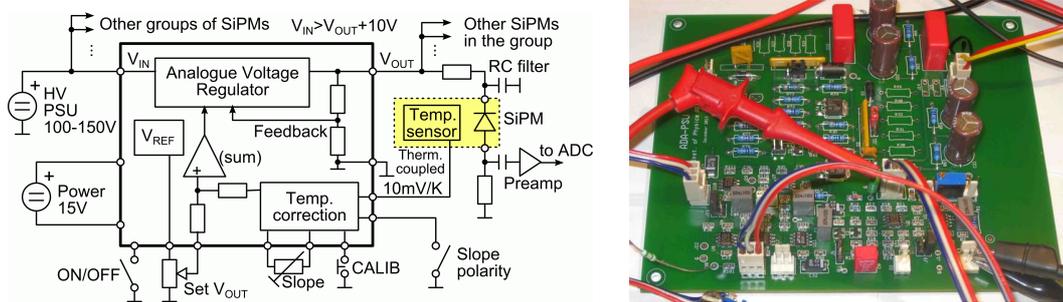


Fig.3. Principal scheme of ADAPower regulator (left) and this circuit on the PCB (right).

5. Results of the gain stabilization

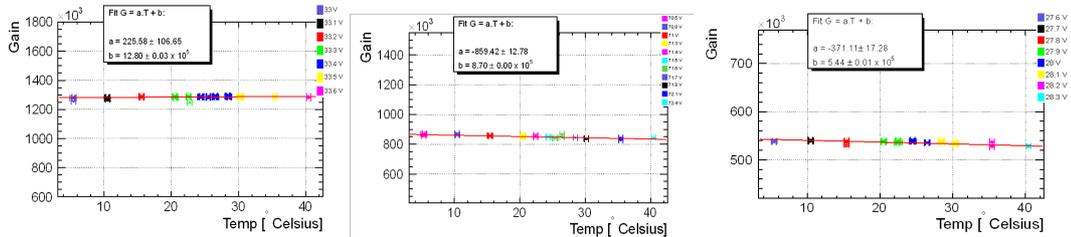


Fig.4. Gain after the compensation for: CPTA1677, Hamamatsu11759 and Ketek W12.

The gain stability of SiPMs with compensation is well below 1%, ranging from 0.05% for Ketek W12 to 0.9% for Hamamastu 11759. Adjustment range for bias voltage is 0.7 V for CPTA & KETEK and 1.9 V for Hamamatsu SiPMs.

6. Conclusion

We measured the dG/dV_{bias} and dG/dT dependences [3] for 15 SiPMs from 3 manufacturers. From these measurements we extracted the compensation slope dV_{bias}/dT . The adaptive power regulator described in this contribution can stabilize the SiPM gain below 1% in the temperature range 5 to 40 °C.

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References

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- [2] PicoTech homepage: <http://www.picotech.com/> .
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