

1 The SST-1M camera for the Cherenkov Telescope 2 Array

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The prototype camera of the single-mirror Small Size Telescopes (SST-1M) proposed for the Cherenkov Telescope Array (CTA) project has been designed to be very compact and to deliver high performance over thirty years of operation.

The camera is composed of an hexagonal photo-detection plane made of custom designed large area hexagonal silicon photomultipliers and a high throughput, highly configurable, fully digital readout and trigger system (DigiCam). The camera will be installed on the telescope structure at the H. Niewodniczański institute of Nuclear Physics in Krakow in fall 2015.

In this contribution, we review the steps that led to the development of the innovative photo-detection plane and readout electronics, and we describe the test and calibration strategy adopted.

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1. Introduction

The Cherenkov Telescope Array (CTA) is an array of ground-based telescopes proposed for studying gamma rays by detecting the UV-blue Cherenkov light generated in atmospheric showers. A sub-array of CTA will be composed of Small Size Telescopes (SST) and will be dedicated to the high energy region of the gamma spectrum, from 5 to 300 TeV. In order to acquire enough statistics at such high energies, the SST sub-array will need to cover a large (several km²) surface on the ground. Key features for the prototyping of the SST telescopes are thus the low cost and the large scale producibility.

The innovative prototype camera proposed for the single mirror Small Size Telescopes (SST-1M) has been conceived with these goals in mind. Among the novelties of the design, the sensor units composing the photo-detection plane are custom produced Geiger-Avalanche PhotoDiodes (G-APD, or Silicon Photomultipliers, SiPM). As demonstrated by the experience with the FACT telescope, a SiPM-based camera can be operated in half moon conditions, thus achieving a 30% longer exposure time if compared to cameras based on Photomultiplier Tubes (PMT) [1]. Moreover, SiPMs do not undergo ageing due to light exposure, are more robust to high intensity light and the working point is more stable in the long term.

2. Camera structure and mechanics

A CAD drawing of the SST-1M camera is shown in figure 1. An innovative feature of the camera is the physical separation between the Photo-Detection Plane (PDP) and the readout and trigger electronics (DigiCam). Both components are enclosed in an aluminium box, with a 3.3 mm thick Borofloat window at the PDP side. The box is shaped into an hexagonal cylinder 90 cm flat-to-flat broad and 60 cm deep. A six petals - six motors shutter installed at the PDP side will ensure the light tightness of the camera at parking position and during dedicated calibration runs, while providing protection from the weather conditions. The camera protection from water and dust is achieved with an IP65 compliant design. The overall structure is very compact and lightweight (less than 200 kg).

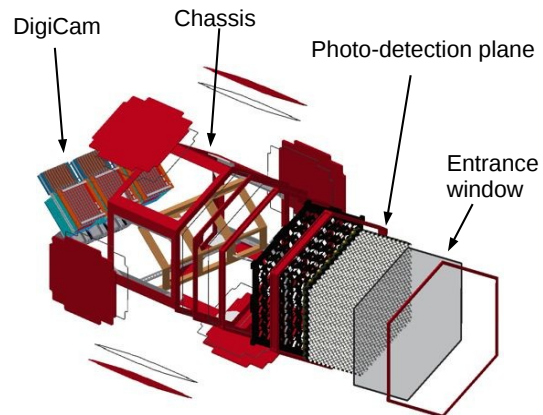


Figure 1: Exploded view CAD drawing of the SST-1M camera.

3. Photo-detection plane

The Photo-Detection Plane (PDP) is a two dimensional array of hexagonal sensor units (the pixels), that is being developed at the University of Geneva. The hexagonal pixel shape is crucial to achieve an optimal trigger performance. Such geometry guarantees that each pixel center is at

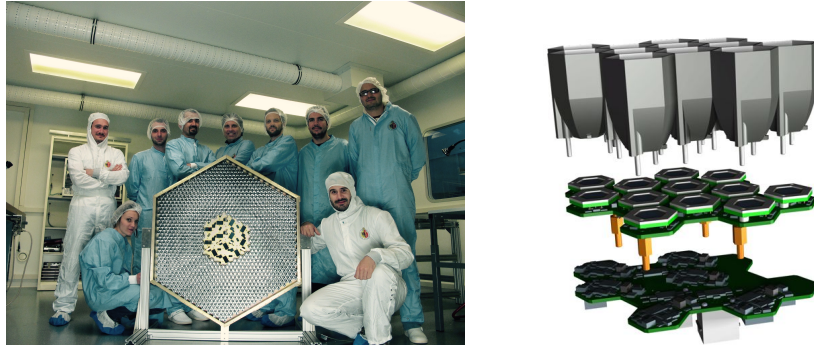


Figure 2: A picture of the PDP (left) assembled in the laboratories of the University of Geneva, except for its central part that was being used for the cooling validation tests the time of the picture, and a drawing of a single 12 pixels module (right) highlighting its main components: the Winston cones, the sensors attached to the PreAmpl board and the Slow Control Board.

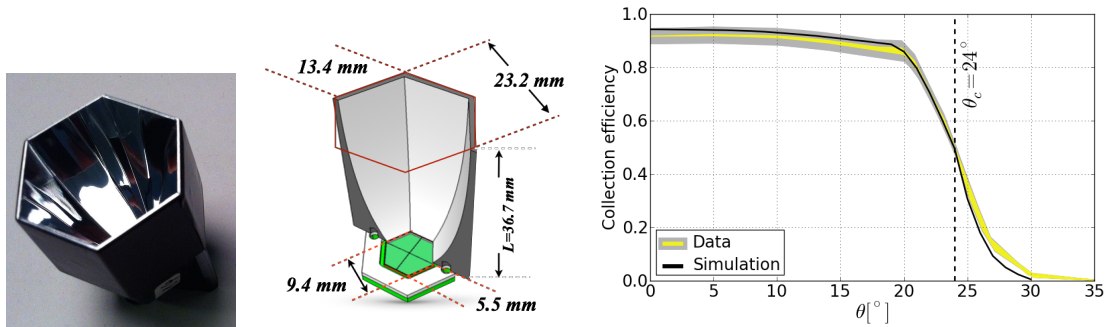


Figure 3: A picture of a single cone (left), a drawing highlighting its geometrical properties with respect to the underlying sensor (center) and the measured light transmission efficiency as a function of the angle of incidence θ , showing the cut-off at 24° (right).

41 the same distance from all its neighbors, so no preferred direction exists and trigger algorithms can
 42 benefit from such a symmetry.

43 The 1296 pixels of the PDP are arranged into 108 individual modules (see figure 2) of 12 pixels
 44 each, directly connected to the front-end electronics boards. The modules are mounted on an
 45 aluminium backplate, that guarantees the mechanical stability of the PDP but also serves as an
 46 active part of the cooling system (see later on). The PDP is 88 cm broad flat-to-flat and weights
 47 35 kg.

48 3.1 Light concentrators

49 The required 9° field of view and few arcmin angular resolution of the SST camera imply, for
 50 single-mirror optics, a possible hexagonal pixel size of 2.32 cm flat-to-flat. Since no SiPM of such
 51 a linear dimension exists, the solution adopted for the SST-1M camera is to use concentrators to
 52 focus the light onto a sensor with smaller area. To match the hexagonal pixel surface to a standard
 53 SiPM with square surface, the only possibility is to use a plastic light guide. However, compressing
 54 the hexagonal pixel entrance into a standard $3 \times 3 \text{ mm}^2$ square sensor would require a quite long
 55 light guide (around 4-5 cm), which would absorb most of the UV light. To avoid this, the use

56 of hollow Winston cones has been proposed [3]. The geometry of the cone is fixed by the pixel
 57 size and by the requirement of having a cut-off for light coming at angles above 24° (to reduce
 58 the stray light coming from directions outside the field of view and to maximize the collection
 59 efficiency of the light focused by the mirror dish on the photosensor), and has been optimized for
 60 the manufacturing process using Bezier curves to shape the six faces of the hexagonal cone (see
 61 figure 3). The cones are made of polycarbonate and are produced using the cheap injection molding
 62 technique. An aluminium and dichroic layers coating covers the inner surface to enhance the UV
 63 reflectivity at large angles of incidence.

64 3.2 SiPM sensors

65 The geometry of the light concentrator
 66 determines the size and shape of the under-
 67 lying sensor as 9.4 mm side-to-side on an
 68 hexagonal surface. Such large area SiPMs,
 69 with such a peculiar shape, are not com-
 70 mercially available, and have thus been de-
 71 signed for the purpose in a collaboration with
 72 Hamamatsu exploiting standard manufactur-
 73 ing techniques for low cross-talk SiPMs [2].
 74 A picture of the sensor, together with a zoom

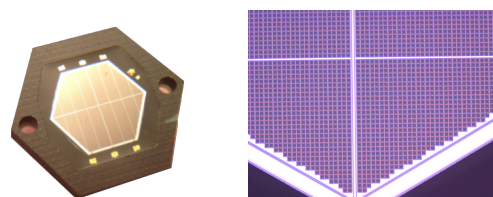


Figure 4: A picture of the large area hexagonal SiPM (left) and a microscope image showing individual microcells (right).

75 on its $50\ \mu\text{m}$ size square cells, is shown in figure 4.

76 The high number of microcells (36840 square cells in total, each of $50\ \mu\text{m}$ size) that can be packed
 77 on the large sensor area ($93.56\ \text{mm}^2$) ensures the operation of the device far from saturation. How-
 78 ever, the large area produces a high capacitance ($3.4\ \text{nF}$), that translates into a long duration of
 79 the signals (in the order to 100 ns), thus limiting the bandwidth capability of the device to below
 80 the required 10 MHz rates. To overcome this limitation, the 36840 cells have been split in four
 81 channels (9210 cells each, $850\ \text{pF}$ capacitance) in common cathode configuration.

82 3.3 Front-end electronics

83 Due to space constraints, the front-end electronics of each module is implemented in two
 84 separate PCBs, the preamplifier board (PreAmp) and the Slow Control Board (SCB). The design
 85 of the two boards has been driven by the need of having a low noise, high-bandwidth, low power
 86 and limited cost front-end electronics.

87 3.3.1 PreAmp board

88 The PreAmp board amplifies and shapes the signals coming from the sensors. In order to
 89 provide a single readout channel per pixel, the signals coming from the four channels of a single
 90 sensor are recombined via the summation circuitry shown in figure 5. Here transimpedance stages
 91 are used to amplify the charge, so that the resulting signals bear information about the amount of
 92 detected light.

93

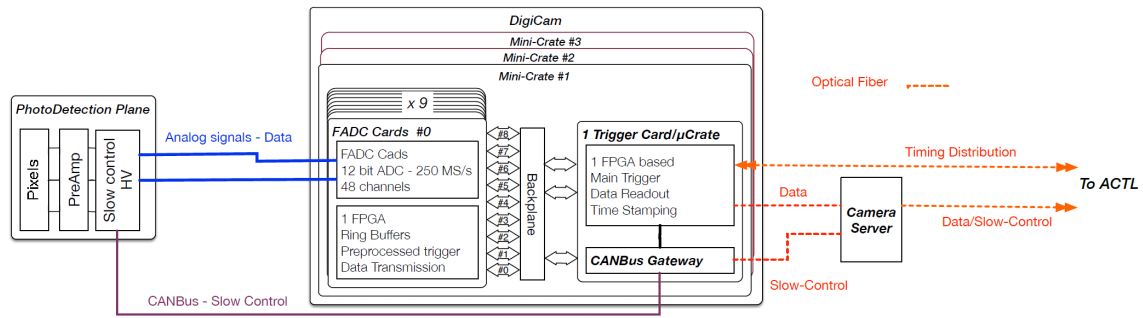


Figure 6: A schematics of the readout architecture.

94 An innovative feature is the DC coupling of
 95 the sensors to the front-end, which will allow
 96 to monitor the baseline position as a function
 97 of the background light level, giving access
 98 to direct information about the night sky
 99 background (NSB). The NSB is foreseen to
 100 be order of 30 MHz per pixel in dark nights,
 101 considerably higher than the dark noise rate
 102 of about 5-10 MHz.

103 3.3.2 Slow Control Board

104 The Slow Control Board (SCB) is a key component of the PDP. Its main functions are to provide the bias voltage to each sensor of the module individually and to route the analog signals from the PDP to DigiCam via standard RJ45 connectors. Furthermore, the SCB implements a complex logic that ensures the stability of the working point of the sensors. In fact, the gain of a SiPM is determined by the overvoltage, i.e. the difference between the bias voltage and the breakdown voltage, which is strongly dependent on temperature. In fact, average gradients in the order of 5 °C are expected over the PDP surface. The design of an active cooling system to stabilize the temperature would be challenging. The adopted solution is to realize a logic that, at a frequency of 2 Hz, stabilizes the working point of each sensor individually as a function of temperature. The temperature is measured from a negative temperature coefficient (NTC) probe that has been implemented in the sensor on purpose, with a precision of 0.17 °C. The compensation loop is implemented in a microcontroller integrated in the SCB, and it also features a real-time control logic that, using a periodic readout of the bias voltage, checks and compensates for rounding errors. The bias voltage is adjusted with a precision of 6.69 mV.
 118 The SCB is accessible via CANbus, which also allows to directly read the temperature and read/write
 119 the bias voltage.

120 4. DigiCam

121 The approach of having a fully digital, dead time free, readout and trigger system, DigiCam,
 122 is another innovative feature of the camera, allowing for high flexibility in the implementation of

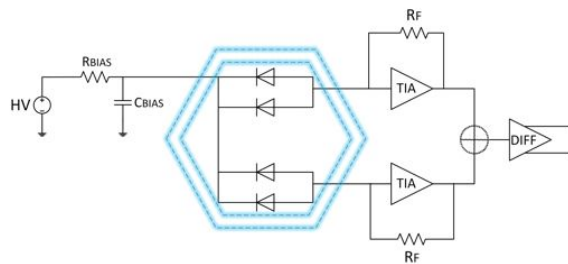


Figure 5: Schematics of the preamplifier.

123 the readout and trigger logic. The DigiCam hardware has been developed by the Jagiellonian Uni-
 124 versity in Kràkow, and consists of 3 minicrates located at the back of the camera, each containing 9
 125 digitizer boards and one trigger board interconnected via a custom backplane. For the readout and
 126 trigger purposes, the PDP is, in fact, logically divided into three sectors, each handled by one of
 127 the minicrates. All the data processing taking place in DigiCam is done by high throughput, latest
 128 generation, FPGAs (Xilinx XC7VX415T) present on each board. The digitizer board hosts 12 fast
 129 ADCs (4-channel AD9239 converters from Analog Devices, Inc.) for the digitization of the signals
 130 coming from 48 pixels, at the sampling rate of 250 MHz. The digitized signals are stored in ring
 131 buffers, and are processed to generate a L0 trigger decision before being sent to the trigger board
 132 located in the same minicrate. Of the three trigger boards, one is configured as master and the
 133 other two as slaves. The trigger boards are in charge of collecting the data on DDR3 memories, of
 134 calculating the L1 trigger decision and of sending the data to the central acquisition system via the
 135 master trigger card on a 10Gb fiber ethernet link. Within each crate, a highly parallelized trigger
 136 algorithm is applied to the data of the corresponding sector plus the neighboring channels of the
 137 adjacent sectors. The memories integrated on the trigger boards can be expanded in order to cope
 138 with the required rates. More information about DigiCam can be found in [4]. A schematics of the
 139 full readout architecture is shown in figure 6.

140 5. Cooling

141 The total power consumption of the camera is
 142 ~ 2 kW, where the contributions of the PDP and of
 143 DigiCam are 500 W and 1.2 kW, respectively. The
 144 cooling system was designed to comply with the IP65
 145 insulation requirement on the camera structure and to
 146 ensure an efficient heat extraction from the very com-
 147 pact electronics.

148 The cooling of the PDP is done via a liquid coolant sys-
 149 tem. Cold water (at 10°C or less, chilled by external
 150 cooling units) flows through aluminium pipes fixed di-
 151 rectly onto the backplate of the PDP, which thus serves
 152 as a heat sink, via aluminium blocks. The four screws
 153 that secure each module to the backplate serve as cold
 154 fingers to extract the heat from the front-end electronics. In order to ease the heat extraction from
 155 both the PreAmp and the SCB and to minimize temperature gradients, the two boards have been
 156 produced with a thicker copper layer ($72\ \mu\text{m}$ instead of the conventional $18\ \mu\text{m}$). Moreover, a ther-
 157 mally conductive filler material is inserted between the boards and between the fully assembled
 158 module and the backplate. The PDP cooling concept has been validated on a 1:10 mock-up of the
 159 PDP and on FEM calculations (see figure 7).

160 The heat is extracted from the DigiCam boards using heat pipes coupled with the water cooling
 161 pipes via a metal block as exchanger. To guarantee the proper functioning of the heat pipes at any
 162 inclination of the telescope, the DigiCam minicrates are mounted at a 45° angle, as shown back in
 163 figure 1.

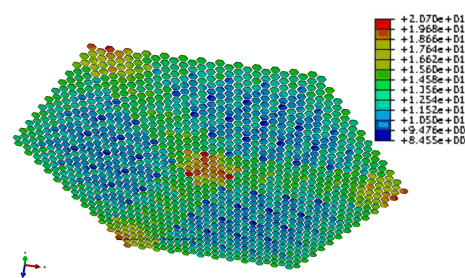


Figure 7: FEM calculation of the PDP temperature when the cooling system uses water at 7°C .

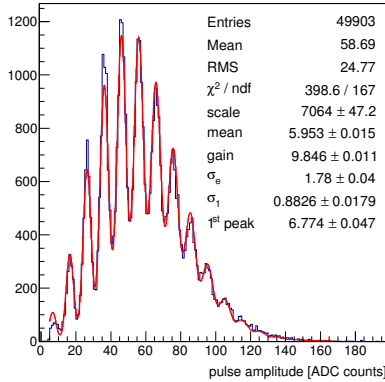


Figure 8: Single photon spectrum of a SiPM for an average of 10 photons illuminating the sensor.

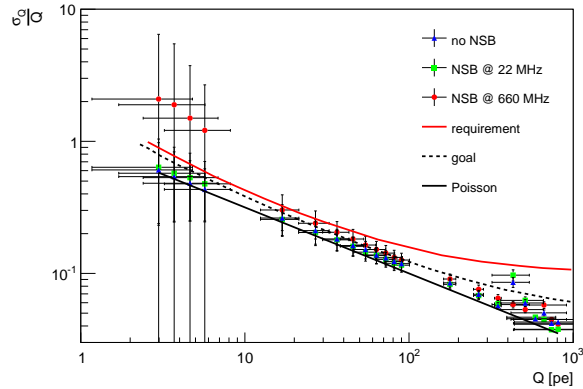


Figure 9: Charge resolution for a single sensor at different Night Sky Background (NSB) levels.

164 6. Production, quality check and assembly

165 In view of a possible large scale production following the prototyping phase, quality check
 166 procedures have been established for the various components prior to the assembly of the camera.
 167 The quality of the cones has been assessed on a subset of the full batch. Due to the high uniformity
 168 measured, there is in fact no need for a one-by-one characterization. The SiPM sensors have been
 169 characterized and tested by Hamamatsu. Dedicated electronics boards and control software have
 170 been realized for testing the PreAmp and the SCB at the production factory, which, for the case of
 171 the SCB, perform a preliminary calibration. Each setup has been designed to be highly portable
 172 and easy to operate. The results of the PreAmp and SCB tests have revealed a very good uniformity
 173 of the production.

174 The assembly of the single 12 pixels modules has been performed at the University of Geneva.
 175 Before being fixed on its final location on the PDP, each module undergoes an optical test using
 176 a dedicated setup. In this system, four modules can be illuminated simultaneously by the light
 177 emitted from a 470 nm LED source and transmitted up to each pixel through optical fibers. Using
 178 both pulsed light and continuous light, the setup allows to characterize the behavior of the modules
 179 in terms of the main signal parameters (e.g. baseline position, peak amplitude, rise time and fall
 180 time) for each sensor, as well as ADC gain uniformity, performance of the compensation loop,
 181 and so on. These data are used to calculate the flat fielding coefficients of the module, and can be
 182 also used in the future in the analysis of the actual data from the detected gamma-ray events (see
 183 also [5]). The level of electronic cross talk has also been measured by illuminating one pixel in a
 184 module and by looking at the signals seen by all the other pixels. The result of the test shows that
 185 induced signals are only visible at high light levels (orders of 3000 photons), and even in this case
 186 the effect is in the order of few per-mill.

187 7. Performance

188 Preliminary studies on the performance of the camera detector units have been carried out.
189 Single sensors or full modules have been tested in terms of optical properties using LED sources.
190 Figure 8 shows as an example the single photon spectrum obtained from one pixel of one of the
191 assembled modules. The separation between the single photo-peaks is good despite the four chan-
192 nels of the SiPM are biased with an average operational voltage, as a consequence of the common
193 cathode configuration. Figure 9 shows the charge resolution measured from one sensor with a
194 relatively large gain spread between channels. Despite this, the charge resolution for dark night
195 conditions (22 MHz NSB) falls below the goal line for almost all the charge range, and even the
196 data points for half moon conditions (660 MHz NSB) are below requirements for light levels above
197 10 photo-electrons, and below the goal above 60 photo-electrons.

198 8. Conclusion

199 With its innovative features, the camera prototype for the SST-1M telescope proposed for the
200 Cherenkov Telescope Array is foreseen to provide a high performance in the detection of air show-
201 ers induced by high energy gamma rays in the atmosphere. While the camera is being assembled
202 and commissioned at the University of Geneva, each of its components (light concentrators, sen-
203 sors, electronics, and so on) is being tested and checked, and all the related data is being stored.
204 This will provide a database of all the relevant operational parameters on a single-pixel basis, that
205 will be helpful in the analysis of the actual data to ensure that the full potential of the camera can
206 be exploited.

207 9. Acknowledgements

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