

PoS

Accelerating Twisted Mass LQCD with QPhiX

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We present the implementation of twisted mass fermion operators for the QPhiX library[†] We analyze the performance on the Intel Xeon Phi (Knights Corner) coprocessor as well as on Intel Xeon Haswell CPUs. In particular, we demonstrate that on the Xeon Phi 7120P the Dslash kernel is able to reach 80% of the theoretical peak bandwidth, while on a Xeon Haswell E5-2630 CPU our generated code for the Dslash operator with AVX2 instructions outperforms the corresponding implementation in the tmLQCD library by a factor of $\sim 5 \times$ in single precision. We strong scale the code up to 6.8 (14.1) Tflops in single (half) precision on 64 Xeon Haswell CPUs.

The 33rd International Symposium on Lattice Field Theory 14–18 July 2015 Kobe International Conference Center, Kobe, Japan

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[†]https://github.com/JeffersonLab/qphix

1. Introduction

The *QPhiX* library [1] offers a collection of highly optimized kernels and inverters to perform Lattice QCD computations on recent Intel x86 systems. QPhiX is a C++ library and consists of two components: the lower level component of the library provides with the code generator which abstracts away vector intrinsics (supporting IMCI, AVX, AVX2, AVX512, SSE and QPX instructions). The high level part is concerned with parallelizing over threads via OpenMP and multi-processing via MPI; it is also performing the loop structure for the cache-blocking strategy. Currently, the library supports Wilson (Clover) fermions and (mixed precicion) CG, BiCGstab inverters. There is also an implementation of the staggered Dslash operator [2]. For details we refer to the original work [1]. In the present study we extend QPhiX for initial support of degenerate twisted mass fermions [3, 4].

2. QPhiX and twisted mass fermions

The degenerate twisted mass Dslash operator D_{TM} is defined as

$$\mathcal{D}_{\mathrm{TM}} = \mathcal{D}_{\mathrm{W}} \mathbf{1}_f + i \mu \gamma_5 \tau_3 \,, \tag{2.1}$$

where D_W is the standard Wilson Dslash operator which is diagonal in flavour space. The second term is the *twisted mass term* which is nontrivial in flavour space (τ_3 being the third Pauli matrix in flavour space), μ is the twisted mass.

QPhiX relies on even-odd preconditioning and therefore the operator (2.1) requires two kernels (plus the corresponding daggered versions) which act on the even (odd) sites separately, in QPhiX parlance, *Dslash* and *AChiMBDPsi*:

1. Dslash:

2. AChiMBDPsi:

with $R = 1 + i2\kappa\mu\gamma_5$ being the twist operator.

The successive application of these kernels corresponds to the Schur decomposed fermion matrix

$$\tilde{M}_{oo} = R_{oo} - \frac{1}{2} \kappa(\not\!\!D_{\mathrm{W}})_{oe} R_{ee}^{-1} (\not\!\!D_{\mathrm{W}})_{eo}$$
(2.4)

which then enters, e.g., the Conjugate Gradient algorithm. We implement these kernels on the level of the code-generator. The high-level part has to be modified to include the twisted mass parameter μ and to call the corresponding twisted mass low-level kernels, apart from that it is equivalent to the pure Wilson case.

The QPhiX routine dslash_plain_body() generates the (Wilson) Dslash kernel routines, within that, after the call of dslash_body(), we call the (inverse) twisted term. This generates files with vector intrinsics of the form, e.g.,

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for the different floating point precisions, SIMD vector lengths, structure of arrays lengths and gauge field compression types (12 or 18). Next, the generated files are wrapped up as template specializations of the form

The procedure for the AChiMBDPsi kernels is done in a close analogy with the plain Dslash ones.

Lastly, we have to consider the unpacking routines for the MPI communication. The spinors are projected to halfspinors before being exchanged at the boundaries of each domain. These packing routines are identical to the Wilson case. However, when unpacking, while accumulating the different directions and before streaming to memory, we still have to apply the twisted mass term and therefore we need twisted mass specific unpacking routines.

3. Performance

3.1 Single node

In Fig. 1 we summarize the performance results of our twisted mass Dslash kernels as well as of the full Conjugate Gradient (CG) inversion algorithm on a single Intel Xeon-Phi 7120P. The results are distinguished by double (DP), single (SP) and half precision (HP) and moreover whether 12 compression¹ has been used or not (otherwise 18 "compression"). Note that the DP and SP 12 compression Dslash kernels reach around 80% of the theoretical peak bandwidth of the Xeon-Phi.

In Fig. 2 we present an equivalent performance plot but on a Dual Socket Xeon Haswell CPU (E5-2630 at 2.4GHz) with kernels that we produced with the QPhiX code generator for AVX2 instructions. The performance gives around 60% of the corresponding Xeon-Phi performance. With the tmLQCD package [5, 6], we reach 28 Gflops in double and 38 Gflops in single precision, respectively, for the twisted mass Dslash operation. (Note that tmLQCD supports neither 12 compression nor half precision arithmetics). Therefore our QPhiX kernels account for speedup factors of $3 \times$ in DP, $4.9 \times$ in SP and $6.7 \times$ in HP (the latter in relation to the tmLQCD SP result), respectively.

3.2 Weak scaling

Fig. 3 and Fig. 4 show the weak scaling behaviour of the Dslash kernel on up to 64 Xeon-Phi 7120P devices and up to 64 Dual socket Haswell E5-2630 CPUs, respectively. These tests have been performed on the "Galileo" cluster² at Cineca, Italy. On both architectures we compare the

¹To overcome the bandwidth bottleneck to some extent, one may transfer only two of the three SU(3) matrix rows (thus 12 parameters instead of 18) and recalculate the third one on-the-fly.

²http://www.hpc.cineca.it/hardware/galileo



Figure 1: Twisted Mass $32^3 \times 64$ Xeon-Phi 7120P.



Figure 2: Twisted Mass $32^3 \times 64$ Dual Socket Xeon Haswell E5-2630 2.4GHz AVX2.

performance to a run with a proxy [1] that has been made available to us by Parallel Computing Lab, Intel Corporation. When running the code with the proxy, the latter will fully occupy one of the compute cores. Loosing one of the 16 cores of the Dual Socket Xeon Haswell is too costly to profit from optimized MPI communication (Fig. 4). On the Xeon-Phi (Fig. 3), on the other hand, dedicating one of the 61 cores for communication pays off and weak scaling is better than without proxy. The kernel reaches 18.2 Tflops on 64 devices.



Figure 3: Weak Scaling Twisted Mass Dslash SP $48^3 \times 96$ per device Xeon-Phi 7120P.

3.3 Strong scaling

In Figs. 5 and 6 we present the strong scaling behaviour of the Dslash kernel on Dual Socket Xeon Haswell CPUs in single (SP) and half precision (HP), respectively, for a range of lattice sizes $(24^3 \times 48, 32^3 \times 64 \text{ and } 48^3 \times 96)$. While the strong scaling behaviour on the Xeon CPU is resonable, the Xeon-Phis require a too large local volume (compared to today's state of the art lattice sizes) to reach high performance. On the largest lattice we reach 6.8 (14.1) Tflops in single (half) precision on 64 nodes.

4. Summary

We have implemented *Dslash* and *AChiMBDPsi* kernels of the twisted mass fermion formulation for the QPhiX library. The code passes the unit tests of the kernels, the full fermion matrix (2.4) and the Conjugate Gradient algorithm. On the Xeon-Phi 7120P the Dslash kernel reaches 80% of the theoretical peak bandwidth and on a Dual Socket Xeon Haswell CPU with AVX2 generated code our QPhiX kernel outperforms the tmLQCD library by a factor of $4.9 \times$ in single precision ($6.7 \times$ when making use of half precision arithmetics). Thanks to Intel's MPI proxy the weak scaling behaviour is good not only on the Xeon CPUs but also on Xeon-Phis. While strong scaling is good on the Xeon Haswell CPUs on our test machine, the Xeon-Phis require too large local volumes for practical purposes at this stage.

Acknowledgments

We are very greatful to Bálint Joó, Dhiraj D. Kalamkar and Karthikeyan Vaidyanathan for sharing the QPhiX library and moreover for helpful discussions and assistance. Support by INFN and SUMA is acknowledged.



Figure 4: Weak Scaling Twisted Mass Dslash SP $32^3 \times 64$ per node Dual Socket Xeon Haswell E5-2630 2.4GHz AVX2.

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Figure 5: Strong Scaling Twisted Mass Dslash SP Compression12 Dual Socket Xeon Haswell E5-2630 2.4GHz AVX2.



Figure 6: Strong Scaling Twisted Mass Dslash HP Compression18 Dual Socket Xeon Haswell E5-2630 2.4GHz AVX2.