A High Dynamic Range ASIC for Time of Flight PET with monolithic crystals

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The HRFlexToT is a 16-channel ASIC for SiPM anode readout designed for Positron Emission Tomography (PET) applications that features high dynamic range (>8 bits), low input impedance, common cathode connection, high speed and low power (≈3.5 mW/ch). The ASIC has been manufactured using XFAB 0.18 µm CMOS technology. The main characteristics of the HRFlexToT, compared to its predecessor, are a new energy measurement readout providing a linear Time Over Threshold (ToT) with an extended dynamic range, lower power consumption and better timing response. Initial measurements show a linearity error below 3%. Single Photon Time Resolution (SPTR) measurements performed using a Hamamatsu MPPC S13360-3050CS (3x3 mm² pixel, 50 µm cell) shows 30% improvement with respect to the previous version of the ASIC, setting this specification in the order of 141 ps FWHM and reducing 3 times power consumption. It is important to highlight that an SPTR of 141 ps FWHM is, according to the best of our knowledge, the best resolution achieved so far for this sensor. Coincidence Time Resolution (CTR) measurements are expected to be performed during 2018.

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1. Introduction

Positron Emission Tomography (PET) is a diagnosis technique that employs a small quantity of a radioactive substance in order to obtain molecular images of a living organism. Advances in PET technology result in regular performance improvements and sensitivity increase. PET can be divided into pre-clinical imaging, organ dedicated imaging and whole-body imaging depending on the application. Spatial resolution is a key parameter for these applications in the performance of the detectors. Pre-clinical and dedicated organ PET imaging request volumetric resolution in the order of 1 mm$^3$. In general, whole body PET imaging can relax this figure. Another motivation for even larger clinical sensitivity is to open the way to a drastic reduction of doses injected to the patient [1]. Thus, the technological implementation of PET requires fast and efficient detectors coupled to fast readout electronics.

This work presents HRFlexToT (High Resolution Flexible Time-over-Threshold) which is a new version of the FlexToT ASIC [2] designed with an extended dynamic range suitable for readout of pixelated and monolithic scintillator crystals in Time-of-Flight (TOF) PET applications [3]. It features 16 channels, high dynamic range, high speed, a low input impedance stage based on a MOSFET current mirror with double feedback loop, common cathode connection, directly coupled input with common-mode voltage control and separated timing and energy measurement output.

This paper is organized as follows. A brief explanation of the new architecture of the HRFlexToT ASIC is detailed in section 2. Section 3 provides preliminary experimental results in terms of time and energy resolution of the chip. Lastly, the paper concludes in section 4 providing some future avenues of research.

2. HRFlexToT architecture

The main improvements of the HRFlexToT, over its predecessor, are a totally new energy measurement readout providing a linear ToT with an extended dynamic range, lower power consumption and better timing response, i.e., better SPTR which is directly translated into better spatial resolution using TOF image reconstruction. The HRFlexToT block diagram is shown in Figure 1.

The patented current division scheme [3] at the very front-end part of the circuit splits the input current into differently scaled copies which are connected to independent current mirrors to provide the different functionalities of the ASIC. Time response is achieved by using a leading edge current comparator just after the input current conveyor, which provides a non-linear Time over Threshold (ToT) response encoding the collected charge of the photo-electrons following a pulse width modulation. Note that the main limitation of these kind of ToT comparators is that the relation between the input charge to be measured, i.e., the number of photo-electrons detected, and the width of the encoded pulse is non-linear. A fast OR between all timing channels can be read out using a differential LVDS output and individual time readout is provided in single-ended output.

The energy measurement block (Figure 2) is the main difference between the HRFlexToT and its predecessor. The previous version was composed by an RC integrator and a constant DC current source. The linear response was achieved by initially charging a capacitor with the incoming signal, then discharging it using a constant DC current and thus providing a linear discharge and lastly feeding this signal into a voltage discriminator. The problem of this method is that for low currents,
the discharge of the capacitor stops behaving linearly since part of the DC current starts flowing through the feedback network of the RC integrator instead of discharging the capacitor. This effect for low currents is translated into a reduction in the linearity and thus reducing the dynamic range (input peak current range is from around 2.5 mA to at least 18 mA).

The new redesigned energy path, which is conceptually similar to a Wilkinson ADC, does not suffer the problem of low currents. It provides a linear ToT output encoding the collected charge in a binary pulse width with an extended dynamic range (input peak current range is from 150-200 µA to at least 35 mA) and allowing a resolution higher than 8 bits. The main increase in terms of dynamic range is achieved by increasing the minimum current that provides a linear energy response. Hence, this new version can distinguish pulses close to the level of the noise, which is around 0.5 ns, up to pulses with an amplitude of 500 ns. The increase of the sensitivity at low currents is important with monolithic crystals where signal is spread among all SiPMs in arrays.

The energy measurement is obtained in four stages. The first stage is a trans-impedance amplifier and a semi-Gaussian shaper, which can be configured to cope with different scintillators and sensors providing around 50-80 ns peaking time. The shaper block is implemented using a CR-RC pulse shaping network which contains a passive pole zero cancellation (CR block) and an active integrator (RC block) and provides an output pulse with a nearly Gaussian shape. The purpose of this block is to “shape” the signal by shortening the decay time of the pulse to minimize the effect of after-pulsing, this effect is achieved by the CR filter. Moreover, the RC filter makes the rising edge of the pulse stretched and thus slew rate requirements in subsequent processing stages are less demanding.

Secondly, a ramp generator starts a linear ramp after detecting the incoming signal with a configurable delay larger than the shaper peaking time. Thirdly, the peak is sampled by a peak detector that can be also configured as a track and hold (PDH), which is based on [4]. Lastly, a rail-to-rail comparator generates a linear ToT energy pulse using the peak detected and the ramp signal as a threshold. Hence, this mechanism provides a linear ToT response since the ramp used as threshold is linear and the amplitude of the incoming signal (encoding the collected charge) follows
a linear relation with respect to the number of photo-electrons detected. Individual control of the threshold at the comparator is available.

![Functional block diagram of the new energy readout scheme.](image)

Different triggers can be used for the energy measurement: a low-level trigger for signals above 10 photo-electrons that allows to filter Dark Count events; the timing signal of the input stage for trigger levels close to the first photo-electron; a cluster high-level or summation trigger (input signal of all channels is added into a single output before the discriminator, as shown in Figure 1) in case signal is spread among several channels; an external trigger for pedestal calibration. The control of the energy acquisition is performed by an asynchronous Finite State Machine (FSM), which drastically reduces cross talk interference of digital signals on the analog circuitry.

3. Experimental Results

A considerable reduction in power consumption, from 10 mW/ch to 3.5 mW/ch, is partly achieved by using XFAB 0.18 µm CMOS technology instead of AMS 0.35 µm HBT BiCMOS technology as was utilized in the FlexToT ASIC. Initial measurements with a Hamamatsu MPPC S13360-3050CS (3x3 mm² pixel, 50 µm cell) show a SPTR of 60 ps sigma, i.e., 141 ps FWHM, which is, according to the best of our knowledge, the best resolution achieved so far for this sensor (Figure 3). Note that the SPTR measurements has been performed with a pulsed laser with 635 nm wavelength and 20 ps jitter.

SPTR measurements performed using a Hamamatsu S13360-3050CS MPPC (3x3 mm² pixel, 50 µm cell) shows 30% improvement with respect to the previous version of the ASIC, setting this specification in the order of 60 ps sigma (141 ps FWHM), as shown in Figure 3 and 4a. Conversion of electrical injected signal (emulating the response of an SiPM) shows a linearity error of the energy measurement around 3%. Initial energy measurements performed with a Na-22 radiation source shows an energy resolution at 511 keV lower than 10% (FWHM) (Fig. 4b), although better results are expected in future measurements.

4. Conclusions and outlook

Preliminary experimental results of the new generation ASIC of the FlexToT family for PET medical imaging shows a promising performance. The HRFlexToT provides a linear ToT with an extended dynamic range, lower power consumption and better timing response. Future work will include more measurements of SPTR and Coincidence Time Resolution (CTR) measurements.
HRFlexToT: High Dynamic Range ASIC for ToF

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Figure 3: (Above) non-linear ToT width distribution and (bottom) delay of arrival time of the first photon for Time output using Hamamatsu MPPC S13360-3050CS (3x3 mm² pixel, 50 µm cell).

(a) Time resolution (SPTR in terms of FWHM) comparing the FlexToT and HRFlexToT ASICs for different over-voltages.

(b) Energy resolution (using FWHM) at 511 keV using an Na22 source and a LFS scintillator crystal 3x3x20 mm³.

Figure 4: Time and energy resolution using Hamamatsu MPPC S13360-3050CS (3x3 mm² pixel, 50 µm cell).

References


