

## AugerPrime Upgraded Electronics

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Since 2015, the Pierre Auger Observatory has been undergoing an important upgrade. It consists of the addition of Scintillator Surface Detectors (SSD) on top of the existing Water-Cherenkov Detectors (WCDs), as well as a small Photo-Multiplier Tube (sPMT) inside the WCDs (both excluding the outer crown of the array), an Underground Muon Detectors (UMD) in the limited higher density area, denominated Infill, where the stations are installed at a distance of 750 m instead of the standard 1.5 km (SD750 and later also in the SD433), and a Radio Detector antenna array (RD) at each of the 1660 surface detector stations. To process the signals of all these detector systems and to increase the dynamic range and time resolution, new electronics, Upgraded Unified Boards (UUBs) have been developed and are being produced and deployed at the Observatory. The combination of all of these new features of the Surface Detector (SD) will provide an enhanced capability for answering the still many open questions related to the nature of ultra-high energy cosmic rays.

In this work the main characteristics, the production and validation chain, the performances and the status of the implementation of the new Upgraded Unified Boards will be illustrated. The first data collected from the already operational upgraded stations in the array will also be presented.

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## 1. Introduction

Since the beginning, the Pierre Auger Observatory[1] gave a strong contribution to the understanding of ultra high-energy cosmic rays (UHECRs). The principal motivation for the upgrade of the Pierre Auger Observatory, AugerPrime[2], is to provide additional measurements to address the following questions:

- The origin of the flux suppression at the highest energies and the measurement of the mass composition beyond the reach of the Fluorescence Detector (FD).
- Search for a proton contribution in the flux suppression region ( $E > 5 \times 10^{19}$  eV), aiming at reaching a sensitivity to a proton contribution as small as 10% in this region, search of point sources and estimation of the physics potential of existing and future cosmic ray, neutrino, and gamma-ray experiments.
- Fundamental particle physics at energies beyond man-made accelerators and studies of extensive air showers and hadronic multiparticle production.

The proposed upgrade consist of: (i) the addition of a plastic scintillator plane (SSD[3]) above the existing Water-Cherenkov Detectors (WCD); (ii) the installation in the Surface Detector (SD) stations of new electronics that will process the signals from all AugerPrime detectors; (iii) the installation of an additional small PMT (sPMT[4]) in the WCD to increase the dynamic range, (iv) the installation of an underground muon detector (UMD[5]) in the existing SD Infill area; (v) Radio Detector (RD[6]) added on top of each water Cherenkov detector. AugerPrime is in the deployment phase for the various detectors. The electronics entered in the production phase. Already 81 boards are in the field; 84 new boards will be sent to Argentina at the end of July 2021, 400 by the end of August.

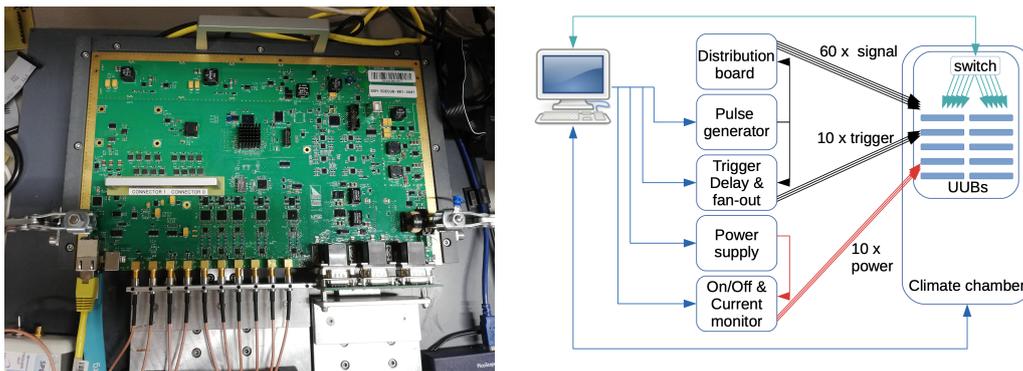
## 2. The Upgraded Unified Boards

The new electronics[7] processes the signals of SSD, SPMT, interfaces UMD and RD and provides a faster sampling of ADC traces for the WCD PMTs. The original Unified Board (UB) will be replaced by the UUB, which is designed to fit the existing enclosure, and accept the existing PMT, GPS antenna, and communication cables. The new electronics implements faster ADCs (120 MHz instead of 40 MHz) with larger dynamic range (12 bits each instead of 10 bits) and a significantly more powerful FPGA. Specifically, it employs a Xilinx Zync-7020 Programmable SoC (Artix-7 FPGA and associated Cortex A9 Dual 333 MHz ARM co-processor). The logic code of AugerPrime is primarily written in IEEE standard synthesizable Verilog. Xilinx Vivado is used for standard modules such as memories, UARTs, and processor bus interfaces. Xilinx PetaLinux, a light LINUX operative system, runs on the embedded ARM processor. The UUB integrates various functions (analog signal processing, triggering, calibration, GPS time tagging, and data acquisition) on a single board. The FPGA is connected to a 4 Gbit LP- DDR2 memory and 2 Gbit flash memory. The FPGA implements in programmable logic basic digital functions like the readout of the ADCs, the generation of triggers, and the interfaces to the LED flasher, GPS receiver, and memories. High-level functions like the data handling and the communications with the radio transmitter are

implemented. The current local station software, originally written in C under OS-9, has been ported to PetaLinux operating system which expands the possible pool of experts for DAQ code development and maintenance. The speed of the upgraded CPU is more than 10 times faster than the previous one, with a similar increase in memory. This allows more sophisticated processing within the local station. The addition of accessible trigger IN/OUT signals, serial terminal, ethernet connection and high-speed USB port facilitates tests both in the lab and in the field.

### 3. Production and Test

The UUB are produced and assembled in Italy by SITAEL SpA<sup>1</sup>. The SlowControl (SC) and Zynq flash are programmed, heat sinks attached and conformal coating applied during production. All the UUBs have a record in the KIT database (containing the UUB production number and the internal serial number). After the validation with prototypes, a sample of pre-production UUBs are taking data since December 2020. The boards, before reaching the field, undergo 2 test phases[8]: 1) Manufacturer test directly in the company where the boards are realised; 2) Environmental Stress Screening (ESS) tests realized in Prague by the local Auger group.



**Figure 1:** Left: UUB in Manufacturer Test system. Right: ESS Test system description.

#### 3.1 Manufacturer test

The manufacturer test, setup in the producer SITAEL company, aims at verifying that all functional blocks of the UUBs are correctly assembled and in operation. The UUB is connected on a stable socket of 160 cm × 80 cm size. The front-end is connected to a pulse generator while the GPS, HV control connectors of the PMTs and the digital interfaces are connected to loop-back adapters (Fig.1 (Left)).

As a first step of the testing procedure, all UUBs should pass an initial automated optical inspection. The inspection allows us to detect problems related to the soldering process (such as excessive or insufficient solder paste) and issues related to component assembly (such as missing components, wrong orientation or distortion of integrated circuits, wrong component polarity) with high efficiency.

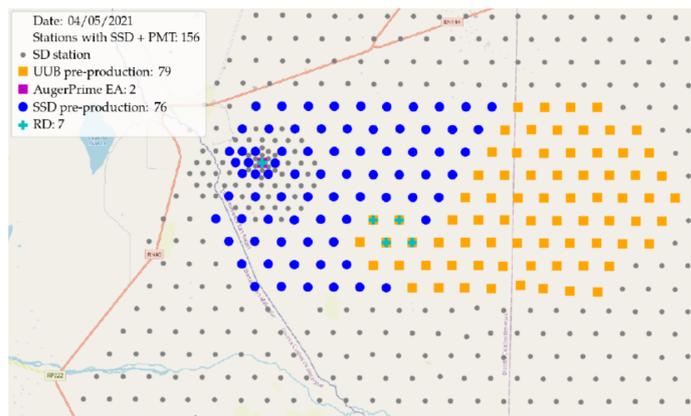
<sup>1</sup><https://www.sitael.com>

Once the automatic procedure is complete, the operator moves the board to the semi-automatic test bench. After connecting all inputs to the test system a script is executed to install the proper operative system and execute the full functionality test. Through a web page it is possible to fill the requested information about components and execute specific tests using an Application Programming Interface (API) running under PetaLinux. After acquiring and analysing the test data, they are formatted and saved in a database to Karlsruhe Institute of Technology (KIT).

### 3.2 Environmental Stress Screening test

The UUBs are sent to the Institute of Physics, Prague after the manufacturing test. The handling of the UUBs follows all the ESD safe precautions foreseen by CEIEN 61340-5-1. The ESS testing is done in three steps: First, preliminary tests are executed to evaluate the noise and general status of each board individually. Next, 10 UUBs at a time are placed inside the climate chamber Binder MKFT115 at a minimum distance 5 cm from each other for the burn-in procedure. Finally, the Environmental Stress Screening is done in 10 cycles, going down to  $-20^{\circ}\text{C}$  and up to  $+70^{\circ}\text{C}$  (temperature change of  $3^{\circ}\text{C}/\text{min}$ ). Thus, the measurement points are:  $20^{\circ}\text{C}$ ,  $0^{\circ}\text{C}$ ,  $-20^{\circ}\text{C}$ ,  $0^{\circ}\text{C}$ ,  $20^{\circ}\text{C}$ ,  $45^{\circ}\text{C}$ ,  $70^{\circ}\text{C}$ . At extreme temperatures the stabilisation and readout time take 15 minutes. During the two last cycles the power ON/OFF test is performed (over/under voltage protection). Further details on the tests performed can be found in [8].

The scheme of the ESS test bench is shown in Fig.1(Right). The communication with the boards is done via Ethernet connection through Mikrotik RB750r2 router placed inside the chamber. The test pulse is issued by AFG3252C (Tektronix), amplified/attenuated and distributed into 60 channels via a custom-made splitter unit. To power the boards, CPX400SP power supply is used, passing through another custom unit allowing monitoring of the current drawn by individual boards and switching the boards off one by one in case of a failure. This unit also provides signals to control the splitter. The last custom-made unit distributes the trigger signal to all ten boards using a suitable delay with respect to the pulse generator.



**Figure 2:** UUB pre-production array

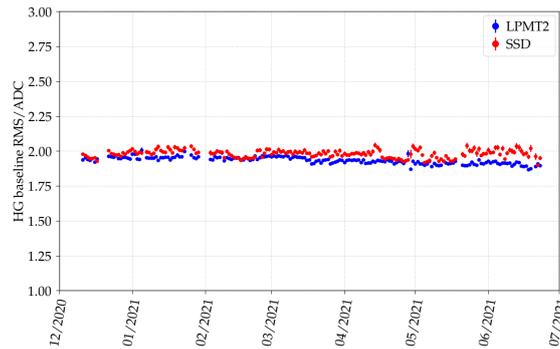
The temperature inside the chamber is monitored by two independent temperature sensors and recorded every 30 seconds together with Zynq temperatures, SC values and the current drawn

by each UUB. All Data are recorded into the official data-base sited in Prague and mirrored to Argentina where, after these tests, the board are sent to be assembled and deployed in the field.

#### 4. Deployment and Performances

In Argentina, the boards undergo a final test before being fully integrated and installed in the field. Actually 81 boards are installed in the field (as shown in Fig.2), 79 in a pre-production engineering array and 2 in the AugerPrime stations Clais and Trak, the 2 stations where all the AugerPrime detectors are already installed and are used to test the AugerPrime integration.

Since the end of 2020, all installed boards are acquired with standard array and processed like old UBs. Currently the UUB trigger signals are filtered at 40MHz to emulate UB, in order to not perturbate the standard DAQ procedure, which in this phase is still dominated by the old electronics. Offline the UUB data are extracted for monitoring and testing purposes. The new devices such as SSD and sPMT, that can be acquired only by the new electronics, are then continuously studied by the collaboration. For each board, the ten ADC channels are verified to have stable baselines both in mean values and RMS. Also the calibration of all detectors are performed and monitored as shown in the following sections.

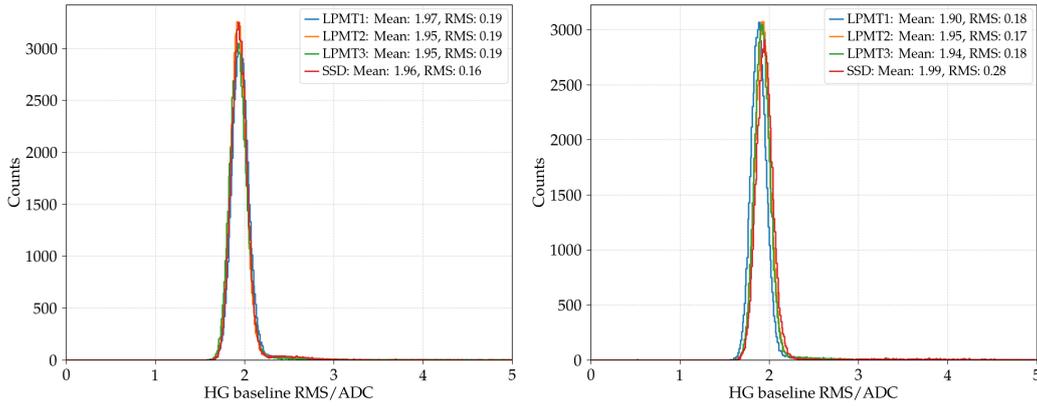


**Figure 3:** Distribution of the daily mean value of the baseline RMS distribution for the HG channel of a LPMT and an SSD channel. The error bar is the RMS of such daily distribution.

##### 4.1 WCD and SSD

In the UUBs, the WCD PMT anode signal is divided in two channels: High Gain (HG) and Low Gain (LG). The HG/LG ratio is 32. The SSD LG channel is attenuated by a factor 4 to extend the dynamic range. So the SSD HG/LG ratio is 128. In Fig.3 the HG RMS of the baseline distribution in function of time is shown for station 22 with UUB implemented. A WCD LPMT and an SSD signals are shown. In all stations the HG channel RMS are less than 2 ADC counts while in LG channels are less than 0.5 ADC counts. The data shown have been taken between January the 1st 2021 and May the 31st 2021. Each point is the daily mean value of the baseline RMS distribution and the error bar is the RMS of such daily distribution.

Also the baseline values are monitored and are very stable (within  $\pm 1$  ADC count). In Fig.4 the distribution of the daily mean value of the baseline RMS for the stations 20 and 22 are shown (Left station 20 and Right station 22 signals). For the Calibration of the WCD PMTs the muon



**Figure 4:** Distribution of the daily mean value of the baseline RMS for the stations 20 (Left) and 22 (Right)

signals are acquired by dedicated triggers and the Vertical Equivalent Muon (VEM) is evaluated using HG channel. The Low Gain channel is calibrated in the range of superposition of signals with HG. The SSD calibration is based on the signal of a minimum-ionizing particle (MIP) going through the detector. About 40% of the calibration triggers of the WCD produce a MIP in the SSD. We are studying the possibility to use SSD data to perform better WCD calibrations. An example of the VEM and MIP calibration histograms is shown in Fig.5. In this figure in the lower right plot the red line indicates the cut to apply on SSD ADC peak to enhance the hump to valley ratio for VEM evaluation in WCD PMTs calibration.

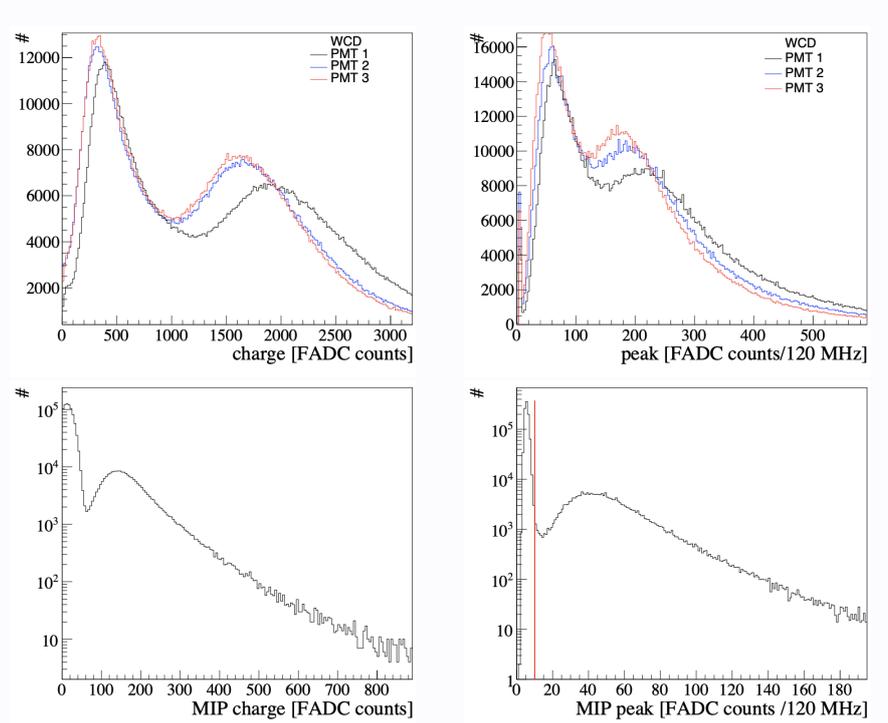
The time resolution has been studied with the prototype UUBs in two tanks (Clais and Trak). The result shows that, in measuring the fixed delay between these two stations with a mutual trigger, the time resolution is  $\sim 5.0$  nanoseconds.

#### 4.2 The small PMT

For the purpose of extending the dynamic range of the signal acquisition, the WCD is equipped with an additional small photomultiplier tube (sPMT), a 1" diameter Hamamatsu R8619, which signals are digitized with 12 bits at 120 MHz in a separate channel. The sPMT features the same bialkali photocathode of the XP1850 model (WCD PMT) but with an active area  $\sim 100$  times smaller, potentially allowing for an equivalent dynamic range extension when operated at the same gain of the WCD PMTs ( $\sim 3 \times 10^5$ ). Adjusting the sPMT gain, the extension can be limited to a factor of  $\sim 32$  allowing the measurement of signals up to the required target of 20,000 VEM without saturation.

Due to its small area, the sPMT cannot be calibrated using atmospheric muons. A dedicated selection of small local shower is set up to this aim, furthermore imposing a minimum threshold of  $\sim 80$  VEM on the WCD PMTs (corresponding to roughly 200 events per hour per station) to guarantee a reasonably large signal on the sPMT, not affected too much by statistical fluctuations. The signals in a region of superposition (which upper limit is given by the WCD PMTs saturation) are then exploited to cross-calibrate the sPMT using the VEM-calibrated signals of the WCD PMTs.

The sPMT cross-calibration factor is monitored and its value updated several times per day in the offline data analysis to follow the variation induced by the temperature dependence of the sPMT

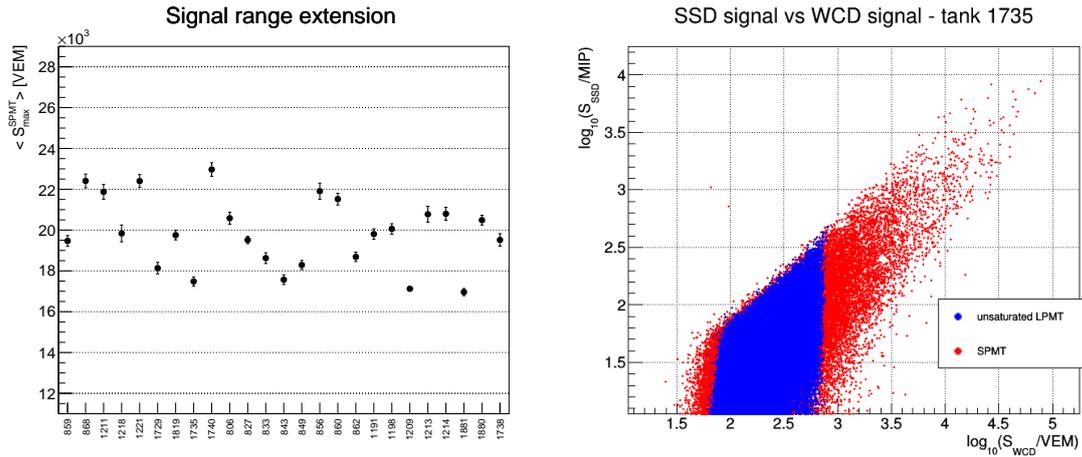


**Figure 5:** Calibration Histograms: Top - histograms show the muon charge (left) and peak (right) for the three WCD PMTs; Bottom - The muon charge (left) and peak (right) in the SSD. The red line in the last plot, indicates the cut to apply on SSD ADC peak to enhance the hump to valley ratio for VEM evaluation in WCD PMTs calibration

gain. The changes in the WCD PMTs gain is instead accounted for by continuously updating the single muon calibration.

The cross-calibration factor is also employed to estimate the maximum signal ( $S_{max}^{sPMT}$ ) that can be measured by each sPMT without saturating the acquisition electronics (since the sPMT linearity is maintained up to much larger particle density inside the WCD). Its average value ( $\langle S_{max}^{sPMT} \rangle$ ) is shown in Fig.6 (Left) for a set of stations where the sPMT high voltage has been manually set to roughly match the requirement of measuring signals up to 20,000 VEM without saturation. The dispersion among the daily estimations results to be of the order of few % for all the analyzed stations, demonstrating the stability of the dynamic range extension when the temperature excursion from day to day is negligible (an average variation of 5 degrees is found in the considered period).

Using the small showers events and the cross-calibration result, the relation between the signals in the WCD and in the scintillator can be examined for each single tank even on a timescale of few weeks, as shown in Fig.6 (Right). The SSD signals are expressed in MIP while the WCD ones in VEM, with the WCD PMTs covering the dynamic range up to the saturation (blue dots) and the sPMT extending it up to a few  $10^4$  VEM (red dots). The signals show a larger dispersion with respect to standard ultra-high energy air showers. Indeed the vast majority of these small showers are estimated to have energy lower than few PeV, producing non uniform distributions of particles over the station and thus very different signals in the WCD and SSD.



**Figure 6:** Left: Expected maximum sPMT signal without saturation for a subset of stations, estimated using 1 month of small showers data. The error bars in each value represent the dispersion among the daily estimations of  $S_{\text{max}}^{\text{sPMT}}$ . Right: Correlation between the WCD and the scintillator signals for one upgraded AugerPrime station, using the small showers selected for the cross-calibration procedure.

## 5. Conclusions

AugerPrime is in its commissioning phase and all detectors are being installed in the field. The UUBs are in production. All the phases have been intensively verified and tested and the process is now in its full active phase. Already 81 boards are in Argentina and at the end of July 2021 84 more will be sent and 400 more are planned at the end of August 2021. Since December 2020, all UUBs in the field are included in the standard DAQ and monitored offline. The performances of the boards are stable and well within the design requirements. The process is going on as expected and we foreseen the completion of the UUB deployment by the end of 2022. Special thanks are due to the local staff involved in the operations.

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